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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/806,902	03/23/2004	Masakatsu Nakai	09792909-5817	2402
26263	7590	03/29/2006	EXAMINER	
SONNENSCHN NATH & ROSENTHAL LLP P.O. BOX 061080 WACKER DRIVE STATION, SEARS TOWER CHICAGO, IL 60606-1080			NGUYEN, MATTHEW VAN	
			ART UNIT	PAPER NUMBER
			2838	

DATE MAILED: 03/29/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/806,902

Applicant(s)

NAKAI, MASAKATSU

Examiner

MATTHEW V. NGUYEN

Art Unit

2838

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-9 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 22 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
 - 2) ☐ Certified copies of the priority documents have been received in Application No. _____.
 - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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1. The disclosure should be carefully reviewed and ensure that any and all grammatical, idiomatic, and spelling or other minor errors are corrected.
2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1 and 6 are rejected under 35 U.S.C. 102(e) as being anticipated by Seki et al. (U.S. pat. No. 6,657,467).

With regard to claims 1 and 6, Seki et al. (i.e., Fig. 1) shows a semiconductor chip comprising a delay monitoring means (11) for finding a critical path delay characteristic for a target circuit (15), a voltage setting signal generating means (12) for generating a voltage setting signal for setting a power supply voltage (14) to be supplied to the target circuit based on the result of monitoring of delay by the delay monitoring means, a voltage setting restricting means (13) for restricting the maximum value of the power supply voltage set in the voltage setting signal to a predetermined value (as recited in claim 1) in accordance with a signal indicating an operation state of the target circuit (as recited in claim 6) (see col. 4, line 55 – col. 5, line 41).

3. Claims 2-5 and 7-9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

None of prior art of record taken alone or in combination shows a first storing means for storing a maximum voltage setting signal for setting the maximum value of the power supply voltage to be restricted, a comparing means for comparing the maximum value of the power supply voltage set by the maximum voltage setting signal stored in the first storing means with the value of the power supply voltage set by the voltage setting signal and outputting the signal having a lower voltage setting (as recited in claims 2-5); or a plurality of first storing means for storing maximum voltage setting signals for setting the maximum value of the power supply voltage to be restricted, a selecting means for selecting a maximum voltage setting signal, and a comparing means for comparing the maximum value of the power supply voltage (as recited in claims 7-9)

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Arimoto et al. (U.S. Pat. No. 5,856,951), Seki et al (U.S. Pat. No. 6,313,622), Kim et al. (U.S. Pat. No. 6,686,788) and Alexander et al. (U.S. Pat. No. 6,693,473) also disclose semiconductor circuits each of which comprises substantial elements as recited in the claims of the instant application.

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew V. Nguyen whose telephone number is (571) 272-2081.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-2800.

Matthew V. Nguyen
MATTHEW V. NGUYEN
PRIMARY EXAMINER